

An image display capable of multilevel display and having a minimal pixel-to-pixel display characteristic variation. The image display having a display area of a plurality of pixels and a signal line for feeding a display signal voltage to the pixels, comprises, in at least one of the plurality of pixels: a memory for storing the display signal voltage entered from the signal line to the pixel; a pixel turn-on period decision section for determining an ON period and an OFF period for an image output in the pixel according to the display signal voltage; and a pixel driver for repeating an ON operation of the image output a plurality of times in one frame.